



# SVSA2800D Series

## HIGH RELIABILITY HYBRID RADIATION TOLERANT DC-DC CONVERTERS

### DESCRIPTION

The SVSA series of high reliability DC-DC converters is operable over the full military temperature range (-55 °C to +125 °C) with no power derating. Paramount to the SVSA series is a magnetic feedback circuit that is radiation immune. Operating at a nominal fixed frequency of 450 kHz, these regulated, isolated units utilize well controlled undervoltage lockout circuitry to eliminate slow start-up problems.

The SVSA series is specifically designed for the harsh radiation environment of space applications. Performance is guaranteed through the use of hardened semiconductor components, radiation lot acceptance testing (RLAT) of non-hardened components and analysis. The SVSA series has been characterized for Total Ionizing Dose (TID) performance including Enhanced Low Dose Rate Sensitivity (ELDRS) and for Single Event Effects (SEE) according to VPT's DLA approved Radiation Hardness Assurance (RHA) plan per MIL-PRF-38534, Appendix G. Characterization is performed at both the component level and at the SVSA series hybrid converter level.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 Class H and Class K and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266    5,790,389    5,963,438    5,999,433  
6,005,780    6,084,792    6,118,673

### FEATURES

- Guaranteed TID Performance to 30 krad(Si) including ELDRS, per VPT's RHA plan specified per MIL-PRF-38534, Appendix G, Level P with 2X margin.
- Characterized to 44 MeV-cm<sup>2</sup>/mg with minor transients only; no dropouts, shutdowns, latch up or burn out.
- High Reliability
- Up to 6 Watts Output Power
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Radiation Immune Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Current Limit and Short Circuit Protection
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Projection Welded Hermetic Package
- High Power Density: > 19 W/in<sup>3</sup>
- Custom Modified Versions May Be Available
- Additional Environmental Screening Available
- Meets MIL-STD-461 Revisions C, D, E and F EMC Requirements When Used With VPT's EMI Filters
- MIL-PRF-38534 Element Evaluated Components
- Worst Case analysis, Stress, Radiation, and Reliability reports available<sup>1</sup>



Figure 1 – SVSA2800D DC-DC Converter (Exact marking may differ from that shown)

<sup>1</sup>Subject to all export restrictions and export regulations including but not limited to the Export Administration and Foreign Assets Control Regulations. Further restrictions may apply contact VPT for details.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	2.5 Watts	Weight (Maximum)	15 Grams
ESD Rating per MIL-PRF-38534	2		

Parameter	Conditions <sup>8</sup>	SVSA2805D			SVSA2807D			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	15	28	50	15	28	50	V
	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	4	6	-	4	6	mA
	No Load	-	45	60	-	45	60	mA
Ripple Current	Full Load <sup>5</sup> , 20Hz to 10MHz	-	30	50	-	30	50	mA <sub>p-p</sub>
	End-of-Life	-	-	100	-	-	100	mA <sub>p-p</sub>
Inhibit Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off <sup>4</sup>		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Voltage <sup>5</sup>	+V <sub>OUT</sub> $T_{CASE} = 25^{\circ}\text{C}$	4.95	5.0	5.05	6.93	7.0	7.07	V
	+V <sub>OUT</sub> $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	4.925	5.0	5.075	6.895	7.0	7.105	V
	+V <sub>OUT</sub> End-of-Life	4.89	-	5.10	6.85	-	7.14	V
	-V <sub>OUT</sub> $T_{CASE} = 25^{\circ}\text{C}$	4.80	5.0	5.20	6.72	7.0	7.28	V
	-V <sub>OUT</sub> $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	4.75	5.0	5.25	6.65	7.0	7.35	V
	-V <sub>OUT</sub> End-of-Life	4.715	-	5.275	6.60	-	7.385	V
Power <sup>3,6</sup>	Total	0	-	5	0	-	5	W
	$\pm V_{OUT}$ Either Output	0	-	3.5	0	-	3.5	W
Current <sup>3,6</sup>	$\pm V_{OUT}$ Either Output	0	-	0.7	0	-	0.5	A
	Ripple Voltage							
Ripple Voltage	$\pm V_{OUT}$ Full Load <sup>5</sup> , 20Hz to 10MHz	-	30	50	-	30	60	mV <sub>p-p</sub>
	$\pm V_{OUT}$ End-of-Life	-	-	100	-	-	100	mV <sub>p-p</sub>
Line Regulation	+V <sub>OUT</sub> $V_{IN} = 16\text{V}$ to $40\text{V}$	-	2	20	-	2	20	mV
	-V <sub>OUT</sub> $V_{IN} = 16\text{V}$ to $40\text{V}$	-	10	200	-	10	200	mV
Load Regulation	+V <sub>OUT</sub> No Load to Full Load <sup>5</sup>	-	5	50	-	2	50	mV
	-V <sub>OUT</sub> No Load to Full Load <sup>5,7</sup>	-	45	200	-	40	200	mV
Cross Regulation	-V <sub>OUT</sub> +Load 70%, -Load 30% +Load 30%, -Load 70%	-	220	450	-	220	450	mV
EFFICIENCY	Full Load <sup>5</sup>	66	71	-	69	73	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	3	-	-	3	W
	Short Circuit	-	-	3	-	-	3	W
CAPACITIVE LOAD <sup>4</sup>	Either Output	-	-	500	-	-	500	$\mu\text{F}$
SWITCHING FREQUENCY		350	450	500	350	450	500	kHz
ISOLATION	500 V <sub>DC</sub> , $T_{CASE} = 25^{\circ}\text{C}$	100	-	-	100	-	-	M $\Omega$
MTBF (MIL-HDBK-217F)	SF @ $T_C = 55^{\circ}\text{C}$	-	830	-	-	830	-	kHrs

See notes next page.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 $V_{DC}$	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	2.5 Watts	Weight (Maximum)	15 Grams
ESD Rating per MIL-PRF-38534	2		

Parameter	Conditions <sup>8</sup>	SVSA2805D			SVSA2807D			Units	
		Min	Typ	Max	Min	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$\pm V_{OUT}$	Half Load to Full Load	-	130	300	-	130	300	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	200	400	-	120	450	$\mu\text{Sec}$
Line Step Output Transient <sup>4</sup>	$\pm V_{OUT}$	$V_{IN} = 16\text{V}$ to $40\text{V}$	-	500	1000	-	400	1000	mV <sub>PK</sub>
Line Step Recovery <sup>2, 4</sup>			-	300	700	-	300	500	$\mu\text{Sec}$
Turn On Delay	$\pm V_{OUT}$	$V_{IN} = 0\text{V}$ to $28\text{V}$	-	10	20	-	10	20	mSec
Turn On Overshoot			-	0	25	-	0	50	mV <sub>PK</sub>

- Notes:
1. Dependant on output voltage.
  2. Time for output voltage to settle within 1% of its nominal value.
  3. Derate linearly to 0 at 135°C.
  4. Verified by qualification testing.
  5. Half load at  $+V_{OUT}$  and half load at  $-V_{OUT}$ .
  6. Up to 70% of the total power or current can be drawn from any one of the two outputs.
  7. 5% Load to Full Load at -55°C.
  8. End-of-Life performance includes aging and radiation degradation and is within standard limits except where noted.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	2.5 Watts	Weight (Maximum)	15 Grams
ESD Rating per MIL-PRF-38534	2		

Parameter	Conditions <sup>8</sup>	SVSA2812D			SVSA2815D			Units	
		Min	Typ	Max	Min	Typ	Max		
<b>STATIC</b>									
INPUT Voltage <sup>4</sup>	Continuous	15	28	50	15	28	50	V	
	Transient, 1 sec	-	-	80	-	-	80	V	
Current	Inhibited	-	4	6	-	4	6	mA	
	No Load	-	45	60	-	45	60	mA	
Ripple Current	Full Load <sup>5</sup> , 20Hz to 10MHz	-	30	50	-	30	50	mA <sub>p-p</sub>	
	End-of-Life	-	100		-	-	100	mA <sub>p-p</sub>	
Inhibit Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V	
Inhibit Pin Open Circuit Voltage <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V	
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V	
UVLO Turn Off <sup>4</sup>		11.0	-	14.5	11.0	-	14.5	V	
OUTPUT Voltage <sup>5</sup>	+V <sub>OUT</sub>	$T_{CASE} = 25^{\circ}\text{C}$	11.88	12.0	12.12	14.85	15.0	15.15	V
	+V <sub>OUT</sub>	$T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	11.82	12.0	12.18	14.775	15.0	15.225	V
	+V <sub>OUT</sub>	End-of-Life	11.66	-	12.30	14.565	-	15.4	V
	-V <sub>OUT</sub>	$T_{CASE} = 25^{\circ}\text{C}$	11.80	12.0	12.20	14.80	15.0	15.20	V
	-V <sub>OUT</sub>	$T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	11.52	12.0	12.48	14.40	15.0	15.60	V
	-V <sub>OUT</sub>	End-of-Life	11.36	-	12.60	14.19	-	15.775	V
Power <sup>3,6</sup>	Total		0	-	6	-	-	6	W
	±V <sub>OUT</sub>	Either Output	0	-	4.2	-	-	4.2	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	0	-	0.35	-	-	0.28	A
	±V <sub>OUT</sub>	Full Load <sup>5</sup> , 20Hz to 10MHz	-	30	50	-	30	50	mV <sub>p-p</sub>
Ripple Voltage	±V <sub>OUT</sub>	End-of-Life	-	-	80	-	-	80	mV <sub>p-p</sub>
	±V <sub>OUT</sub>		-	-	80	-	-	80	mV <sub>p-p</sub>
Line Regulation	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	2	20	-	2	20	mV
	-V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	10	200	-	10	200	mV
Load Regulation	+V <sub>OUT</sub>	No Load to Full Load <sup>5</sup>	-	2	50	-	2	50	mV
	-V <sub>OUT</sub>	No Load to Full Load <sup>5,7</sup>	-	40	200	-	40	200	mV
Cross Regulation	-V <sub>OUT</sub>	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	220	450	-	220	450	mV
EFFICIENCY		Full Load <sup>5</sup>	72	77	-	73	79	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>		-	-	3	-	-	3	W
	Short Circuit		-	-	3	-	-	3	W
CAPACITIVE LOAD <sup>4</sup>		Either Output	-	-	500	-	-	500	μF
SWITCHING FREQUENCY			350	450	500	350	450	500	kHz
ISOLATION		500 V <sub>DC</sub> , $T_{CASE} = 25^{\circ}\text{C}$	100	-	-	100	-	-	MΩ
MTBF (MIL-HDBK-217F)		SF @ $T_C = 55^{\circ}\text{C}$	-	830	-	-	830	-	kHrs

See notes next page.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

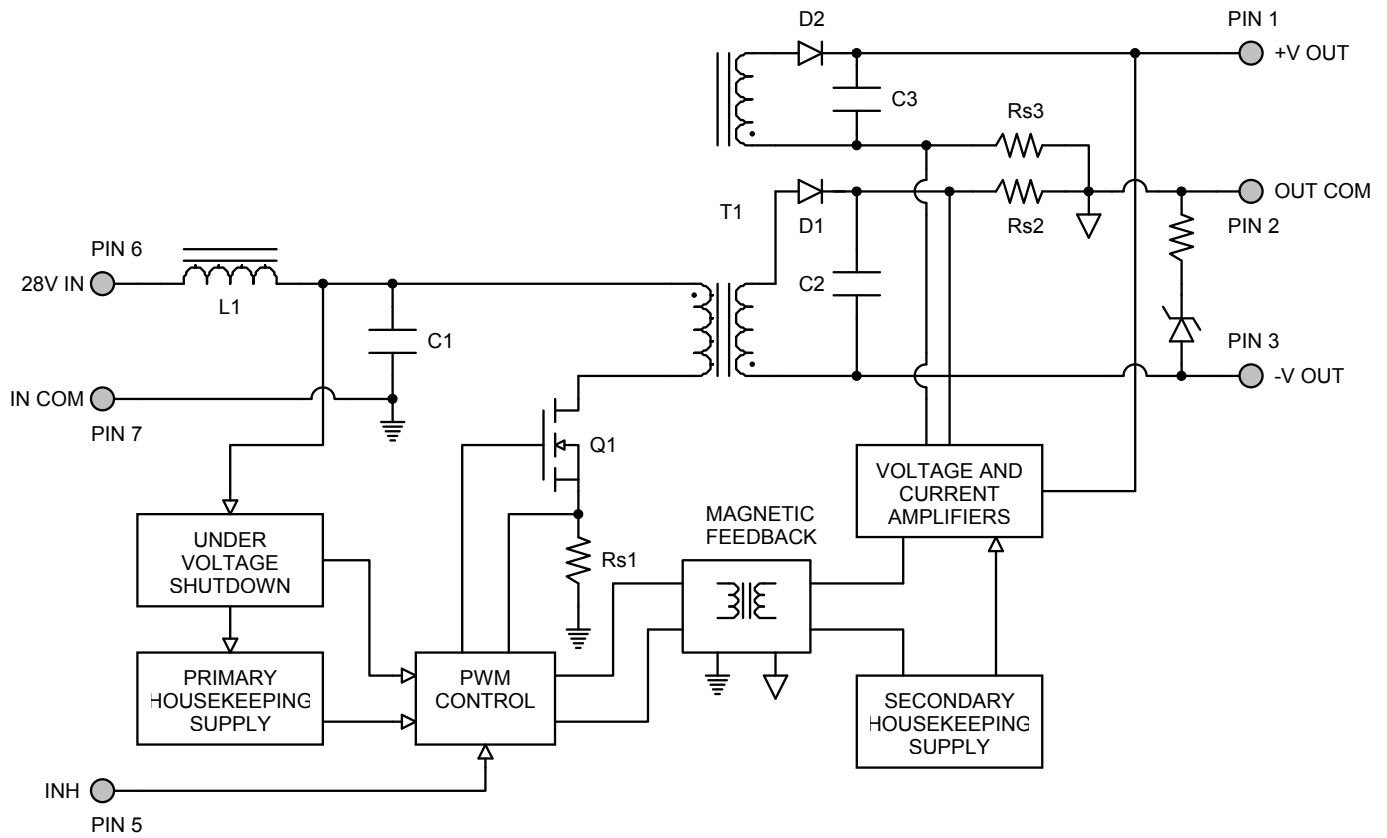
## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 $V_{DC}$	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	2.5 Watts	Weight (Maximum)	15 Grams
ESD Rating per MIL-PRF-38534	2		

Parameter	Conditions <sup>8</sup>	SVSA2812D			SVSA2815D			Units	
		Min	Typ	Max	Min	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$\pm V_{OUT}$	Half Load to Full Load	-	120	300	-	130	300	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	100	450	-	100	500	$\mu\text{Sec}$
Line Step Output Transient <sup>4</sup>	$\pm V_{OUT}$	$V_{IN} = 16\text{V}$ to 40V	-	600	1200	-	500	1200	mV <sub>PK</sub>
Line Step Recovery <sup>2, 4</sup>			-	300	500	-	300	500	$\mu\text{Sec}$
Turn On Delay	$\pm V_{OUT}$	$V_{IN} = 0\text{V}$ to 28V	-	10	20	-	10	20	mSec
Turn On Overshoot			-	0	50	-	0	50	mV <sub>PK</sub>

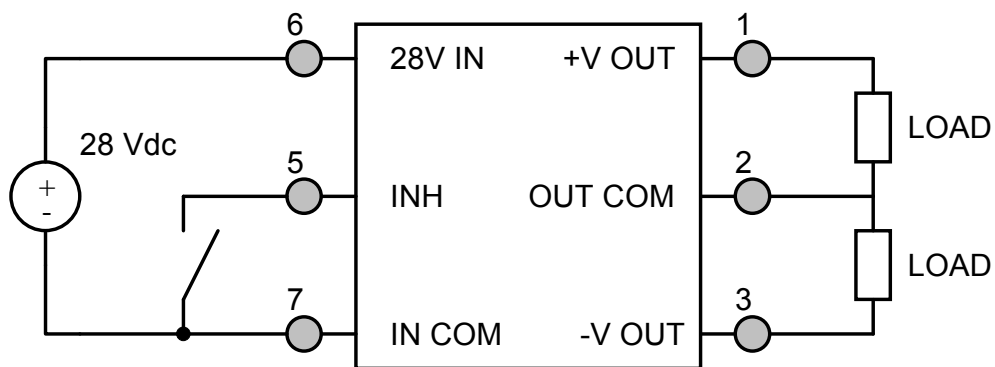
- Notes:
1. Dependant on output voltage.
  2. Time for output voltage to settle within 1% of its nominal value.
  3. Derate linearly to 0 at 135°C.
  4. Verified by qualification testing.
  5. Half load at  $+V_{OUT}$  and half load at  $-V_{OUT}$ .
  6. Up to 70% of the total power or current can be drawn from any one of the two outputs.
  7. 5% Load to Full Load at -55°C.
  8. End-of-Life performance includes aging and radiation degradation and is within standard limits except where noted.

**BLOCK DIAGRAM**



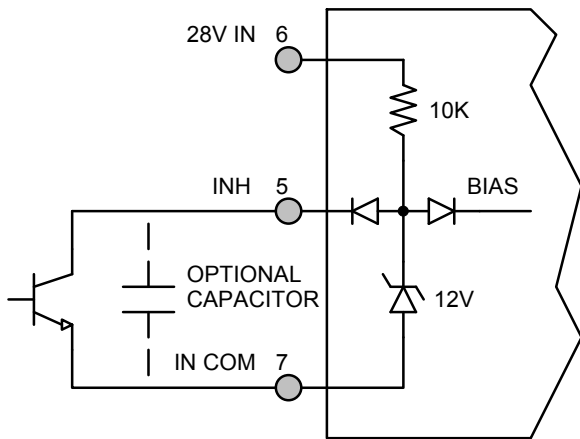
**Figure 2**

**CONNECTION DIAGRAM**

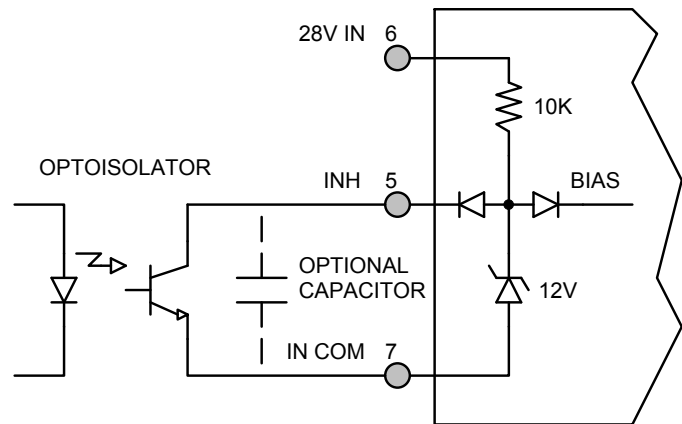


**Figure 3**

INHIBIT DRIVE CONNECTION DIAGRAMS

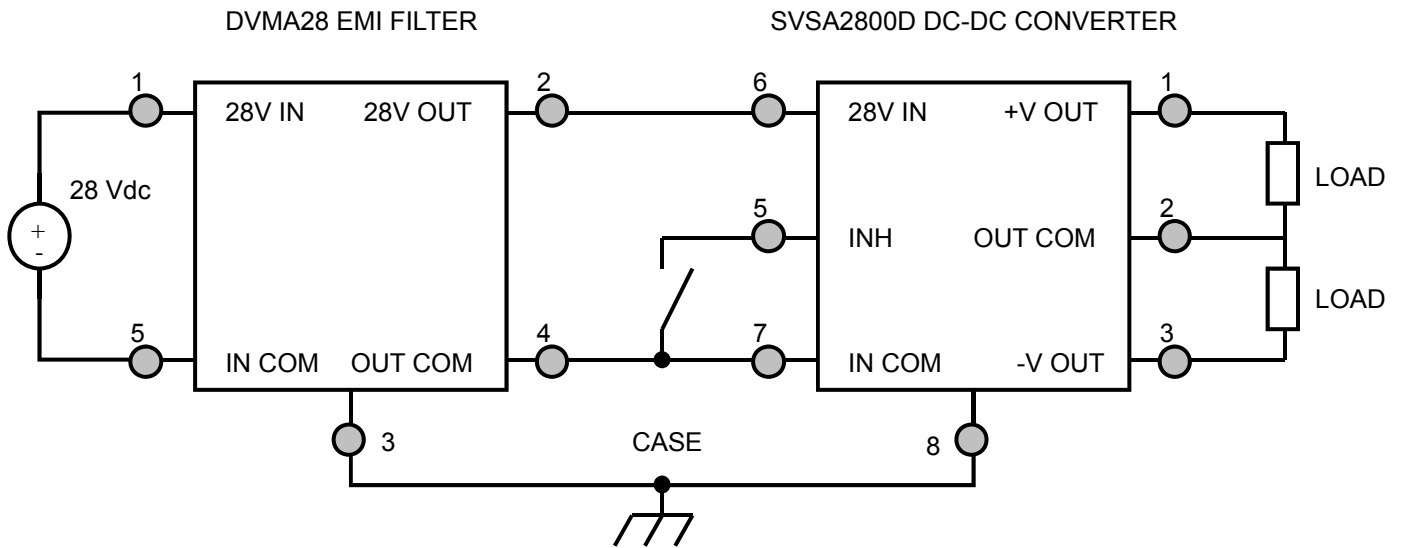


**Figure 4** – Internal Inhibit Circuit and Recommended Drive  
(Shown with optional capacitor for turn-on delay)



**Figure 5** – Isolated Inhibit Drive  
(Shown with optional capacitor for turn-on delay)

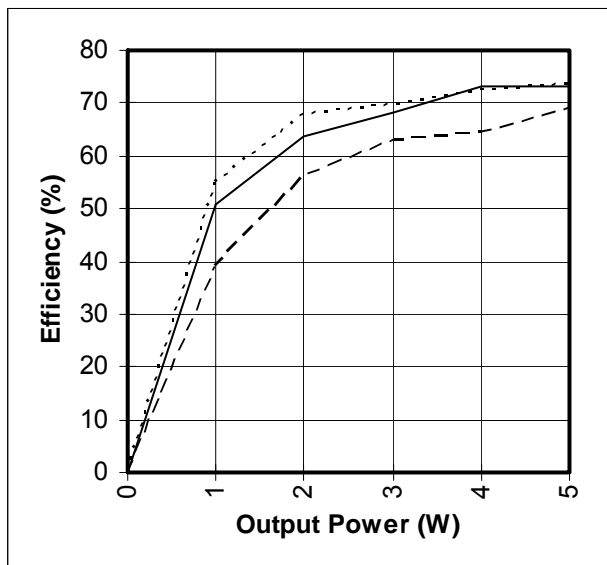
EMI FILTER HOOKUP DIAGRAM



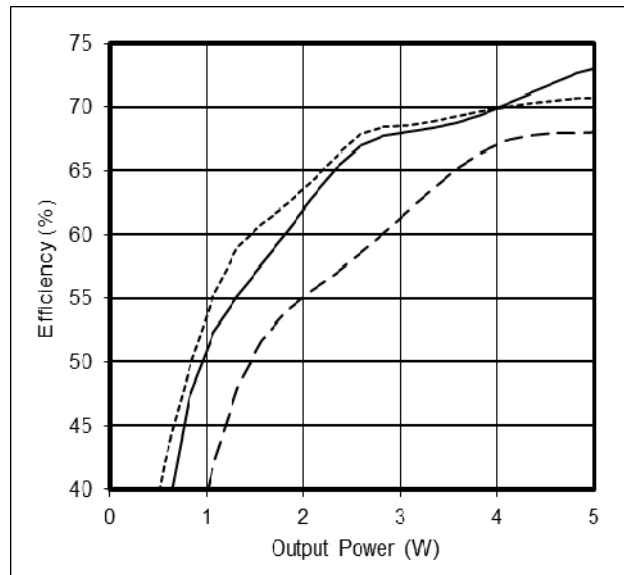
**Figure 6** – Converter with EMI Filter

EFFICIENCY PERFORMANCE CURVES ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)

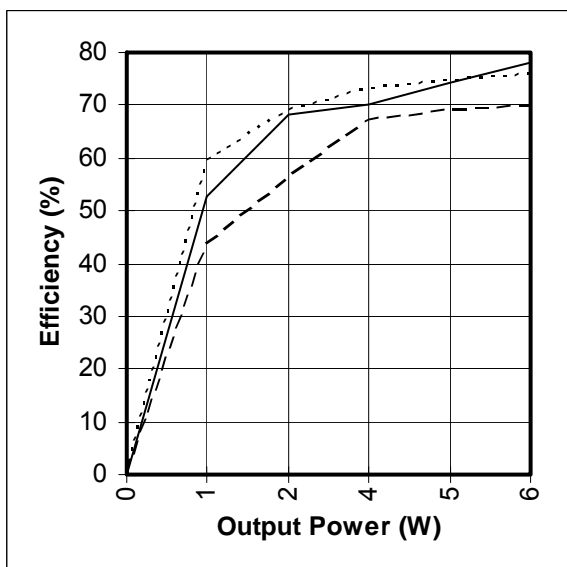
-----  $V_{IN} = 15V$       ———  $V_{IN} = 28V$       - - - - -  $V_{IN} = 50V$



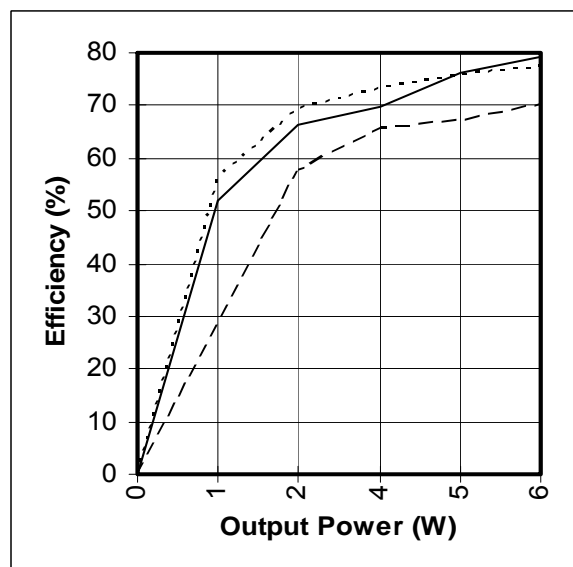
**Figure 7 – SVSA2805D**  
Efficiency (%) vs. Output Power (W)



**Figure 8 – SVSA2807D**  
Efficiency (%) vs. Output Power (W)



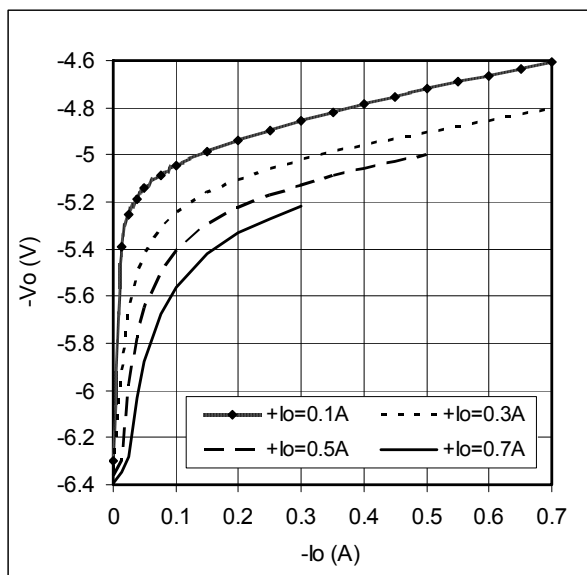
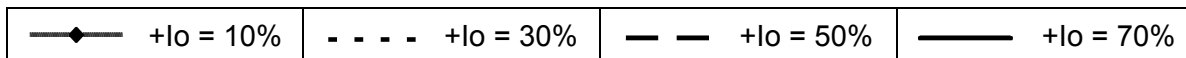
**Figure 9 – SVSA2812D**  
Efficiency (%) vs. Output Power (W)



**Figure 10 – SVSA2815D**  
Efficiency (%) vs. Output Power (W)

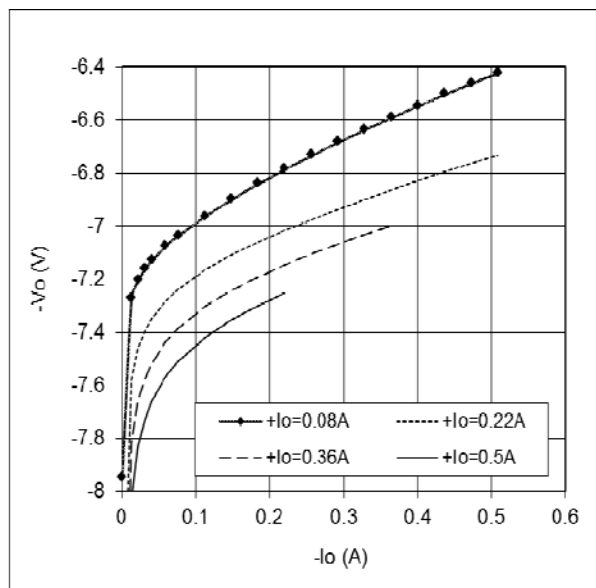


CROSS REGULATION CURVES ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)



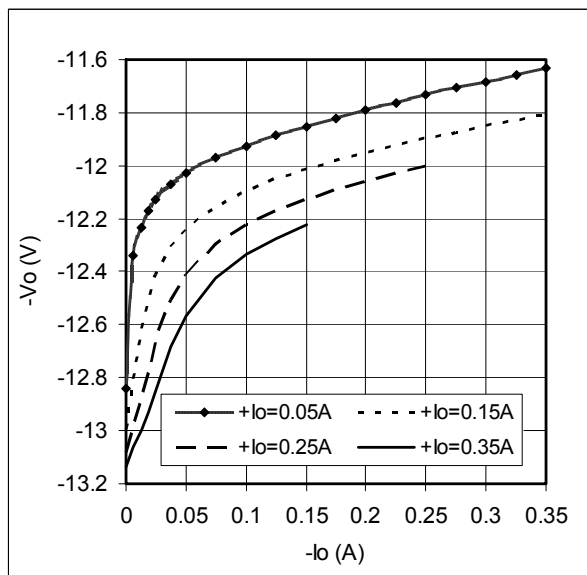
**Figure 11 – SVSA2805D**

-Vout (V) vs. -Iout (A)



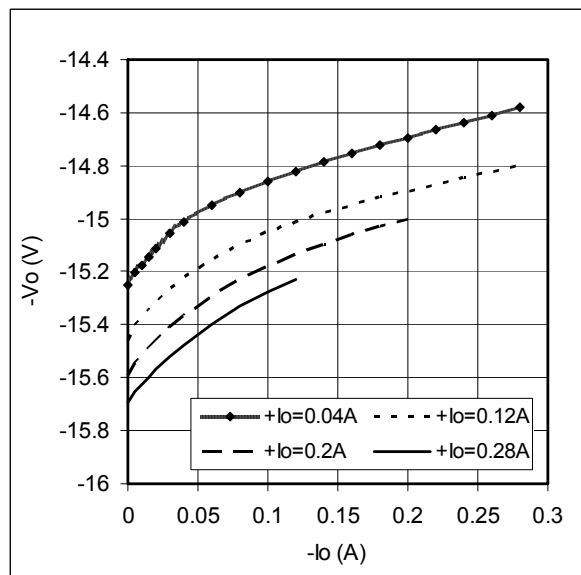
**Figure 12 – SVSA2807D**

-Vout (V) vs. -Iout (A)



**Figure 13 – SVSA2812D**

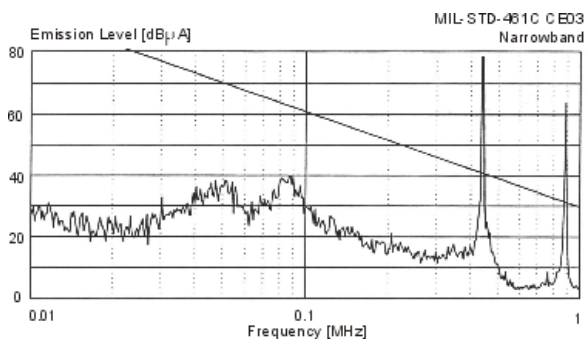
-Vout (V) vs. -Iout (A)



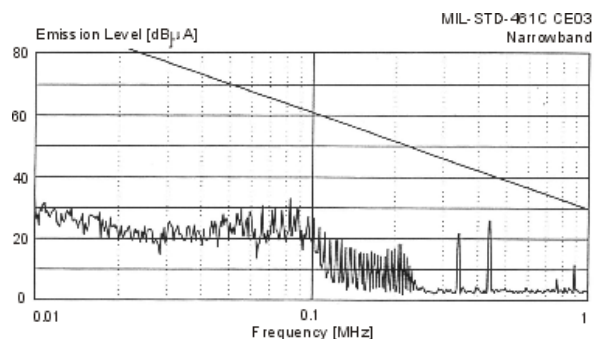
**Figure 14 – SVSA2815D**

-Vout (V) vs. -Iout (A)

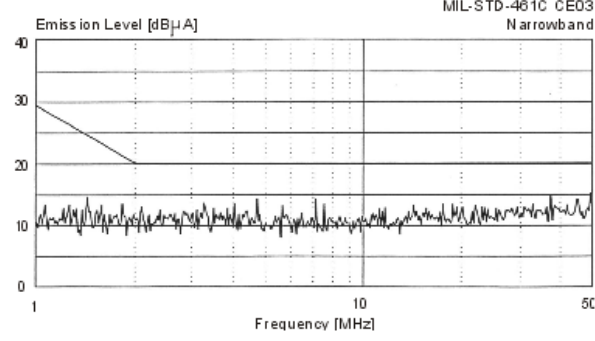
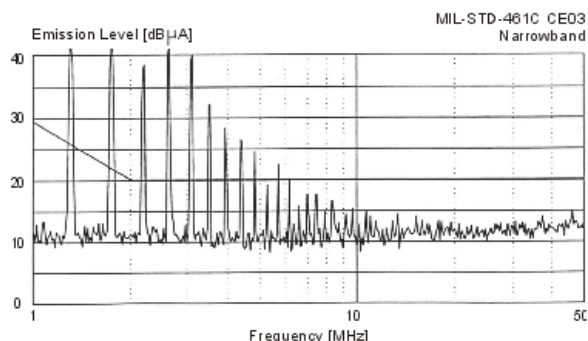
## EMI PERFORMANCE CURVES ( $T_{CASE} = 25^{\circ}C$ , $V_{IN} = +28V \pm 5\%$ , Full Load, Unless Otherwise Specified)



**Figure 15** – SVSA2800D without EMI Filter



**Figure 16** – SVSA2800D with EMI Filter



## RADIATION HARDNESS ASSURANCE (RHA)

The SVSA series radiation performance is guaranteed through the use of hardened semiconductor components, radiation lot acceptance testing (RLAT) of non-hardened components, and characterization of the completed hybrid according to VPT's Radiation Hardness Assurance (RHA) plan per MIL-PRF-38534, Appendix G. Post radiation end of life performance limits are determined by worst case analysis.

As part of qualification, one representative model of the hybrid converter family is characterized for total ionizing dose (TID). TID is tested to 60 krad(Si). Subsequent performance is guaranteed to 30 krad(Si) by 2 times margin. Characterization is performed at high dose rate (HDR) in accordance with condition C (minimum dose rate of 30 rad(Si)/s) of method 1019 of MIL-STD-883, and at low dose rate (LDR) in accordance with condition D of method 1019 of MIL-STD-883. A minimum of 1 biased sample and 1 unbiased sample is tested. After radiation exposure, converter testing is performed at 25 °C per standard datasheet limits. The radiation exposure test circuit is given in Figure 15.

Also as part of qualification, one representative model of the hybrid converter family is characterized for Single Event Effects (SEE). The specific test LET is specified on the first page of the datasheet and is tested to a minimum fluence of  $1 \times 10^6$  particles/cm<sup>2</sup>. The characterization is performed at nominal input voltage at 25 °C in air. The radiation exposure test circuit is specified in Figure 16.

RADIATION HARDNESS ASSURANCE (continued)

Continued compliance is guaranteed by component testing and analysis. Critical semiconductor components, unless procured with manufacturer radiation guarantees, are subjected to RLAT at HDR in accordance with condition C of method 1019 of MIL-STD-883. Semiconductors which have been shown to exhibit ELDRS are subject to RLAT at LDR in accordance with condition D of method 1019 of MIL-STD-883. RLAT is not performed on inherently radiation hard semiconductor component technologies including zeners, diodes, and small signal BJTs.

RHA TEST CIRCUIT DIAGRAMS

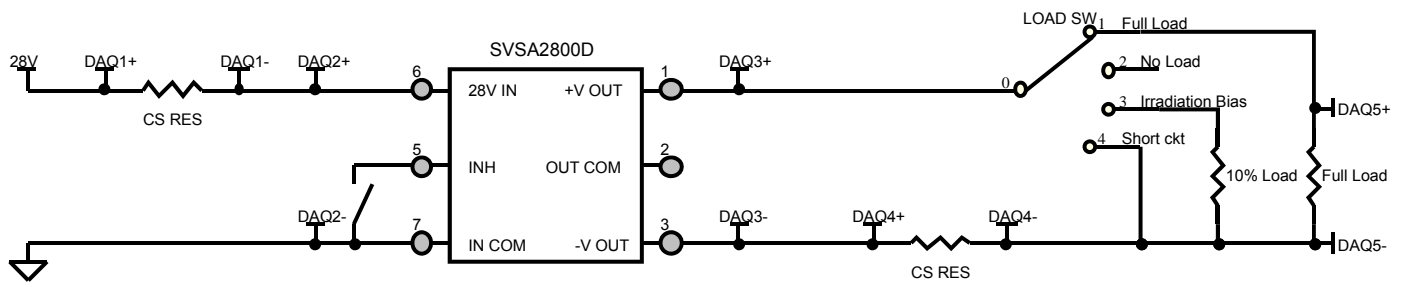


Figure 17 – Radiation exposure circuit (TID)

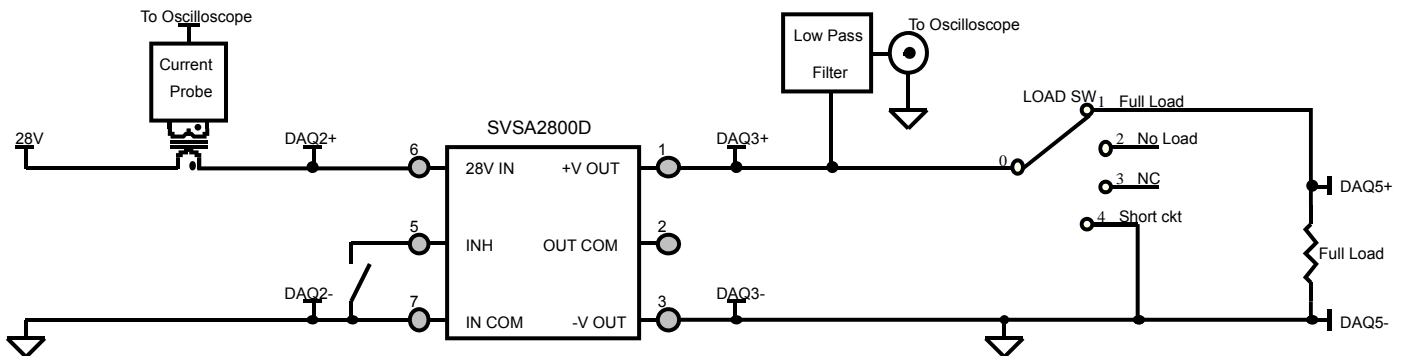
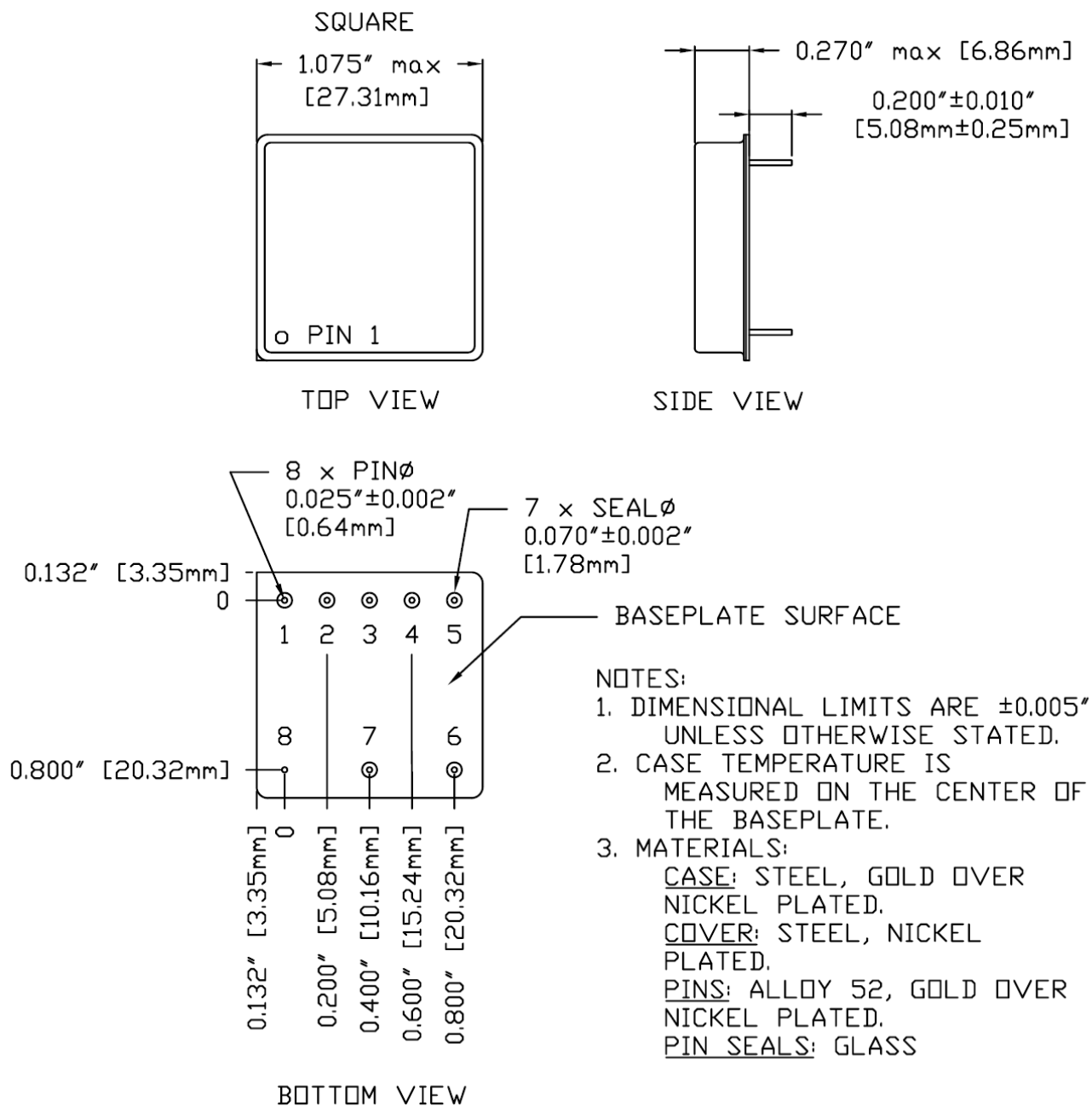


Figure 18 – Radiation exposure circuit (SEE)

PACKAGE SPECIFICATIONS



Pin	Function	Pin	Function
1	+V OUT	5	INHIBIT
2	OUT COM	6	28V IN
3	-V OUT	7	IN COM
4	N/C	8	CASE

Figure 19 – Package and Pinout

## PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	+V OUT	Positive Output Voltage Connection
2	OUT COM	Output Common Connection
3	-V OUT	Negative Output Voltage Connection
4	N/C	No Connection
5	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown. Logic High = Enabled Output. Unconnected or open collector TTL.
6	28V IN	Positive Input Voltage Connection
7	IN COM	Input Common Connection
8	CASE	Case Connection

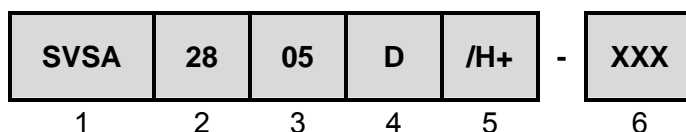
## ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Test	MIL-STD-883 Test Method, Condition	/H+ (Class H + PIND)	/K (Class K)	/EM (Engineering Model) Non-QML ⑤⑥
Non-Destructive Bond Pull	TM2023	• ④	•	• ④
Internal Visual	TM2010, TM2017, TM2032 (MIL-STD-750, TM2072, TM2073)	•	•	•
Temperature Cycling	TM1010, Condition C -65°C to 150°C, Ambient	•	•	
Constant Acceleration	TM2001, 3000g, Y1 Direction	•	•	
PIND ⑦	TM2020, Condition A	• ④	•	
Pre Burn-In Electrical	25°C		•	
Burn-In	TM1015, 320 hrs, 125°C, Case Typ		•	
	TM1015, 160 hrs, 125°C, Case Typ	•		
	24 hrs, 125°C, Case Typ			•
Final Electrical	MIL-PRF-38534, Group A Subgroups 1-6 -55°C, 25°C, 125°C ③	•	•	
	MIL-PRF-38534, Group A Subgroups 1 and 4 25°C			•
Hermeticity (Seal)	TM1014, Fine Leak, Condition A2	•	•	
	TM1014, Gross Leak, Condition C	•	•	
	Gross Leak, Dip ( $1 \times 10^{-3}$ )			•
Radiography ⑧	TM2012		•	
External Visual	TM2009	•	•	•

Notes:

- ① Contact Sales for more information concerning additional environmental screening and testing options desired.
- ② VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
- ③ 100% R&R testing with all test data included in product shipment.
- ④ Not required per MIL-PRF-38534. Test is performed for additional product quality assurance.
- ⑤ Non-QML products may not meet all requirements of MIL-PRF-38534.
- ⑥ Engineering models utilize only the screening specified and are not considered compliant for flight use.
- ⑦ PIND test Certificate of Compliance included in product shipment.
- ⑧ Radiographic test Certificate of Compliance and film(s) or data CD included in product shipment.

## ORDERING INFORMATION



(1) Product Series	(2) Nominal Input Voltage		(3) Output Voltage		(4) Number of Outputs	
SVSA	28	28 Volts	05 07 12 15	± 5 Volts ± 7 Volts ± 12 Volts ± 15 Volts	D	Dual

(5) Screening Code	(6) Additional Screening Code
/EM /H+ /K	Engineering Model Class H+ Class K  Contact Sales

**Note:** Engineering models utilize only the standard screening specified and are not considered compliant for flight use. These models are intended for low volume engineering characterization. The customer must place the following statement on each line item of their purchase order(s) for /EM units when ordering engineering models:

**“(Customer Name) acknowledges that the /EM unit listed in this line item is not permitted for flight use and will be used for Engineering characterization only.”**

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

## SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	SVSA2800D Series Similar Part Number
5962P1121601HXC 5962P1121601HXA 5962P1121601KXC 5962P1121601KXA	SVSA2805D/H+ SVSA2805D/H+-E SVSA2805D/K SVSA2805D/K-E
*T.B.D.	SVSA2807D/H+ SVSA2807D/H+-E SVSA2807D/K SVSA2807D/K-E
5962P1121602HXC 5962P1121602HXA 5962P1121602KXC 5962P1121602KXA	SVSA2812D/H+ SVSA2812D/H+-E SVSA2812D/K SVSA2812D/K-E
5962P1121603HXC 5962P1121603HXA 5962P1121603KXC 5962P1121603KXA	SVSA2815D/H+ SVSA2815D/H+-E SVSA2815D/K SVSA2815D/K-E

Do not use the SVSA2800D Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMDs can be downloaded from the DLA Land and Maritime (Previously known as DSCC) website at <http://www.dsccl.dla.mil/programs/smcr/>. The SMD number listed above is for standard gold-plated lead finish and "P" RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.

## CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone:** (425) 353-3010  
**Fax:** (425) 353-4030  
**E-mail:** [vptsales@vpt-inc.com](mailto:vptsales@vpt-inc.com)

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